

Application No.: 09/874,163

Docket No.: JCLA7083

REMARKS**Present Status of the Application**

Applicants appreciate that claims 11-12 are consider to be allowable.

The Office Action rejected claims 2-10 and 13 under 35 U.S.C. 103(a), as being unpatentable over Hamilton et al. (U.S. Patent No. 6,516,381) in view of Applicant's Admitted Prior Art (AAPA) page 2. Applicants have amended claims 2 and 8 to improve clarity and cancelled claims 7 and 13. After entry of the foregoing amendments, claims 2-6 and 8-10 remain pending in the present application, and reconsideration of those claims is respectfully requested.

Discussion of Office Action Rejections

The Office Action rejected claims 2-10 and 13 under 35 U.S.C. 103(a), as being unpatentable over Hamilton et al. (U.S. Patent No. 6,516,381, "Hamilton" hereinafter) in view of Applicant's Admitted Prior Art (AAPA) page 2. Applicants respectfully traverse the rejections for at least the reasons set forth below.

1. First, the Office Action refers to FIG. 1 ref. 9 of Hamilton as the "*control*" signal of the present invention, and Refs. 9 and 10 as the "*voltage adjustment signal*". Applicants respectfully disagree.

In FIG. 1 of Hamilton, the signal 9 may be consider as the control signal. However, the voltage 10, supplied to the memory module 2, should be the configured operation voltage to the memory module (col. 1, liens 56-60). In more detail (col. 2, line 62 – col. 3, line 2; Fig. 2), the

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voltage from the voltage source 42 is regulated by the regulator 25, and then is supplied to each of the memory modules 27 to 29. In other words, the Refs. 9 and 10 in Fig. 1 are not the voltage adjustment signal of the present invention. *Hamilton failed to disclose the voltage adjustment signal of the present invention.*

2. Further still, Hamilton failed to disclose the system power state signal, and therefore failed to disclose “*outputting a voltage adjustment signal according to the control signal and the system power state signal*” either.

3. Even though AAPA discloses the STD and STR modes, as discussed above, Hamilton is at least in missing the features of the voltage adjustment signal and the system power state signal. Even in combination of Hamilton with AAPA, the prior art still failed to equally disclose the features as recited in claimed invention about “*having the system power state signal entering a low logic state when the computer system enters a STD mode, a soft off mode or a mechanical power off mode*”.

Therefore, independent claims 2 and 3 should be allowable.

4. With respect to independent claim 8, further, both AAPA and Hamilton failed to disclose the “*recognition apparatus*” in operation that “*the recognition apparatus outputs a voltage adjustment signal after receiving the control signal and the system power state signal, so that the voltage control circuit outputs the configured operation voltage to the memory, wherein*

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the system power state signal enters a low logic state when the computer system enters a STD mode, a soft off mode or a mechanical power off mode, and the recognition apparatus receives the system power state signal and then drives the voltage control circuit to output the preset voltage to the memory”.

With at least the same reasons applied to claims 2-3 and the additional reasons, independent claim 8 is also distinguishable over the prior art.

For at least the foregoing reasons, Applicant respectfully submits that independent claims 2, 3 and 8 patently define over the prior art references, and should be allowed. For at least the same reasons, dependent claims 4, 5, 6, 9, 10, 11 and 12 patently define over the prior art as well, wherein claims 11-12 have been considered to be allowable.

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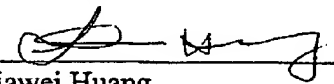
CONCLUSION

For at least the foregoing reasons, it is believed that the pending claims 2-6 and 8-12 are in proper condition for allowance. If the Examiner believes that a telephone conference would expedite the examination of the above-identified patent application, the Examiner is invited to call the undersigned.

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